

<b>Notice of References Cited</b>	Application/Control No. 10/064,041		Applicant(s)/Patent Under Reexamination YANG, CHUN-CHIH	
	Examiner Ayal I. Sharon		Art Unit 2123	Page 1 of 1

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*	B	US-6,502,233	12-2002	Vaidyanathan et al.	717/101
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	K	US-			
	L	US-			
	M	US-			

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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Qualis Design Corp., "Verilog HDL Quick Reference Card." © 1995.
	V	Qualis Design Corp., "VHDL Quick Reference Card." © 1995.
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	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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